

UNIVERSITY COLLEGE OF ENGINEERING (A)
OSMANIA UNIVERSITY, HYDERABAD – 500007

B.E.- V- SEMESTER (MAIN) PRACTICAL EXAMS - DECEMBER-2023
(TIME TABLE)

DEPARTMENT – ELECTRICAL & ELECTRONICS ENGINEERING

Name of the Lab:

(Revised)

Date	Time	Hall Tickets Nos. From ----- To	Strength
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POWER ELECTRONICS LAB

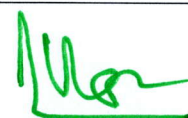
19.12.2023 (Tuesday)	08.00 – 11.00 hrs.	1005-21-734055 to 4057, 4059 to 4062	(07)
	11.00 – 14.00 hrs.	1005-21-734063, 4068, 734081 to 734086	(08)
	14.00 – 17.00 hrs.	1005-21-734087 to 4092 & backlog	(06)
20.12.2023 (Wednesday)	08.00 – 11.00 hrs.	1005-21-734001 to 4008	(08)
	11.00 – 14.00 hrs.	1005-21-734009 to 4012,4014,4015, 4017	(07)
	14.00 – 17.00 hrs.	1005-21-734018 to 4024, 4027	(08)
21.12.2023 (Thursday)	08.00 – 11.00 hrs.	1005-21-734028, 4029, 734031 to 4036	(08)
	11.00 – 14.00 hrs.	1005-21-734037 to 4039, 4041 to 4044	(07)
	14.00 – 17.00 hrs.	1005-21-734046, 4047, 4049 to 4054	(08)

MICRO PROCESSORS AND MICRO CONTROLLERS LAB

22.12.2023 (Friday)	10.00 – 13.00 hrs.	1005-21-734001 to 4012, 4014, 4015, 1005-21-734017 to 4019	(17)
	14.00 – 17.00 hrs.	1005-21-734020 to 4024, 4027 to 4029 1005-21-734031 to 4039	(17)
23.12.2023 (Saturday)	10.00 – 13.00 hrs.	1005-21-734041 to 4044, 4046, 4047, 1005-21-734049 to 4057, 4059 to 4061	(18)
	14.00 – 17.00 hrs.	1005-21-734062, 4063, 4068, 4081 to 4092	(15)

Note :

1. Any omission or clash in the practical's timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.
2. These practical's timetable is also applicable for the **backlog/supplementary** students whose numbers are not listed above.



Chief Superintendent (Exams)
UCE (A), OU.